

AMENDMENTS TO THE CLAIMS

A3 5 1. (Currently amended) A method for making a polysilicon film on a semiconductor wafer, the surface of the semiconductor wafer comprising a plurality of particles, the method comprising:

performing a two-step polysilicon deposition process, the two-step polysilicon deposition process comprising:

10 a first step amorphous silicon (α -Si) deposition process utilizing a low temperature; and

a second step polysilicon deposition process utilizing a high temperature;

wherein the first step amorphous silicon (α -Si) deposition
15 process is used to avoid nucleation of the polysilicon film growth, which ~~utilizes~~ growing by way of the particles on the surface of the semiconductor wafer, so as to inhibit occurrences of needle-like particles and defects on the surface of the polysilicon film.

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2. (Currently amended) The method of claim 1 wherein before performing the two-step polysilicon deposition process, at least one photolithography process, one wet etching process, one photoresist stripping process, one wet cleaning process
25 and one thermal oxidation process are performed on the surface of the semiconductor wafer.

3. (Original) The method of claim 2 wherein the wet etching process comprises a buffer oxide etchant (BOE) etching process
30 and a SC-1 cleaning process.

4. (Currently amended) The method of claim 2 wherein the wet

~~etching~~cleaning process comprises a megasonic scrubbing process, a SC-1 cleaning process, and a SC-2 cleaning process.

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5 5. (Original)The method of claim 1 wherein the particles comprise micro particles, organic substances, metallic particles adhered to the semiconductor wafer, and micro-defects.

10 6. (Original)The method of claim 1 wherein a temperature of the first step amorphous silicon deposition process ranges from 550 to 650°C, and a thickness for the amorphous silicon layer is approximately 100Å.

15 7. (Original)The method of claim 1 wherein a temperature of the second step polysilicon deposition process ranges from 680 to 710°C.

20 8. (Currently amended)The method of claim 1 wherein the two-step ~~polysilicon~~ deposition process is performed in single wafer type low pressure chemical vapor deposition (LPCVD) equipment.

25 9. (Currently amended) A method for forming a polysilicon film on a semiconductor wafer, a surface of the semiconductor wafer comprising a first gate oxide area and a second gate oxide area, the method comprising:

forming a first gate oxide layer (GOX layer) on the surface of the semiconductor wafer;

30 performing a photolithography process and an etching process to remove the first gate oxide layer on the surface of the second gate oxide area;

performing a cleaning process; and

performing a two-step polysilicon deposition process to form a polysilicon layer, the polysilicon layer covering the first gate oxide area and the second gate oxide area;

wherein the two-step polysilicon deposition process comprises a first step low temperature amorphous silicon (α -Si) deposition process to avoid formation of particles and defects by inhibiting nucleation during the formation of the polysilicon layer, and a second step high temperature polysilicon deposition process.

10. (Original) The method of claim 9 wherein the etching process is a wet etching process.

11. (Original) The method of claim 10 wherein the wet etching process utilizes a buffer oxide etchant (BOE) as an etching solution.

12. (Original) The method of claim 9 wherein the cleaning process is a wet cleaning process.

13. (Original) The method of claim 12 wherein the wet cleaning process comprises:

- performing a megasonic scrubbing process;
- performing a SC-1 cleaning process; and
- performing a SC-2 cleaning process.

14. (Original) The method of claim 9 wherein a temperature of the first step low temperature amorphous silicon deposition process ranges from 550 to 650°C.

15. (Original) The method of claim 9 wherein a temperature of the second step high temperature polysilicon deposition process

ranges from 680 to 710°C.

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16. (Currently amended) The method of claim 9 wherein the two-step ~~poly~~silicon deposition process is performed in single wafer type low pressure chemical vapor deposition (LPCVD) equipment.

17. (Original) The method of claim 9 wherein the defects comprise needle-like defects.
